

Intel® Math Kernel Library (Intel® MKL) Parallel Direct Sparse Solver for Clusters

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Agenda

- **Problem statement**
- **Algorithm description**
- **Interface description**
- **Experiments**
- **Conclusions**

Problem statement

$$Ax=b$$

✓ Cons

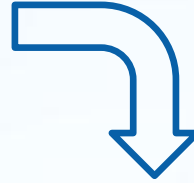
- No extra info available on the matrix, only few generic properties (positive definite, Hermitian,...)
- Huge size

✓ Pros

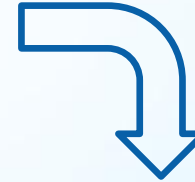
- Clusters with modern CPUs
- Intel® Math Kernel Library (Intel® MKL) with optimized BLAS, LAPACK, PARDISO functionality

Algorithm ($Ax=b$)

Matrix reordering and
symbolic factorization

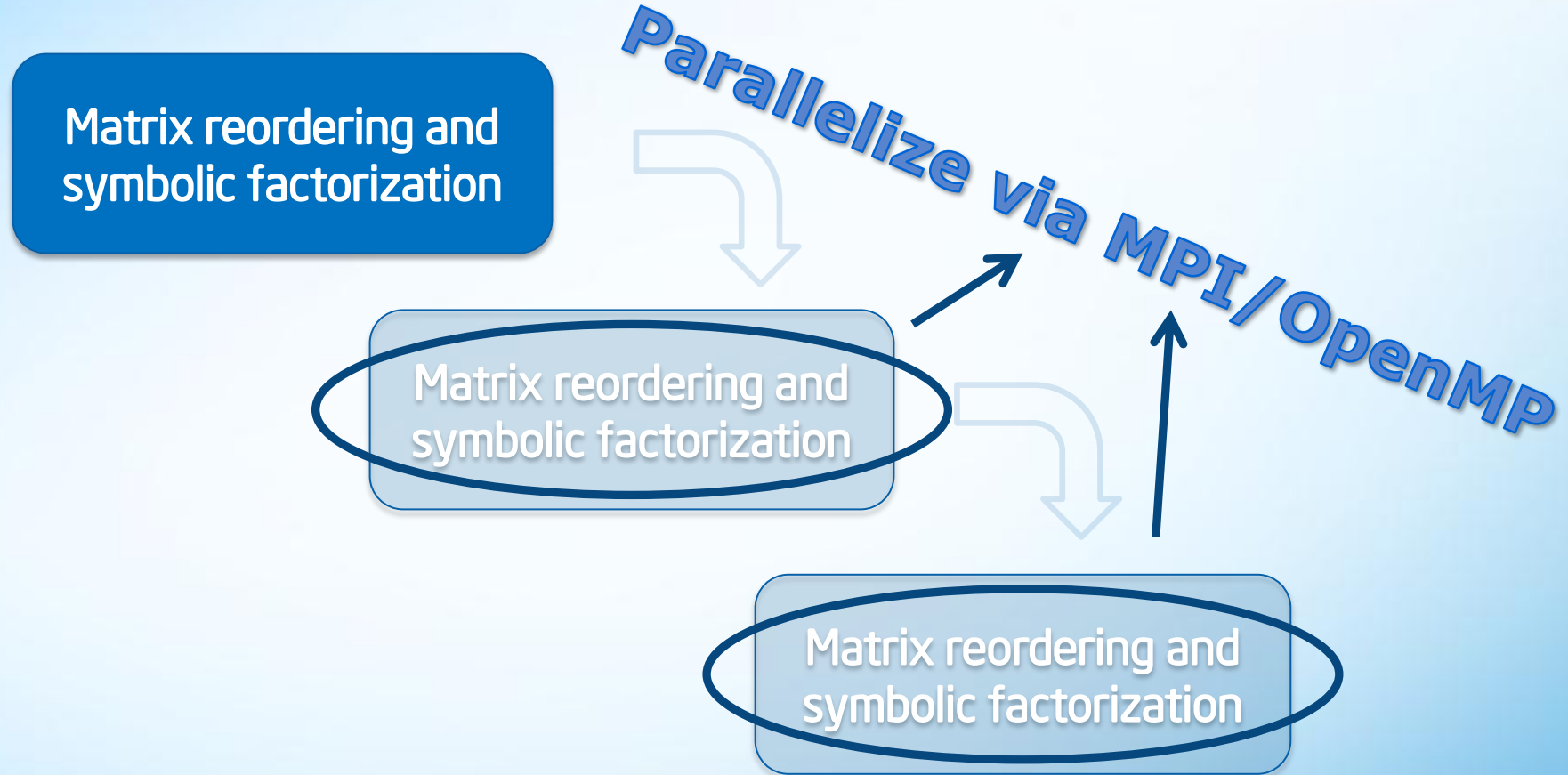


Matrix reordering and
symbolic factorization

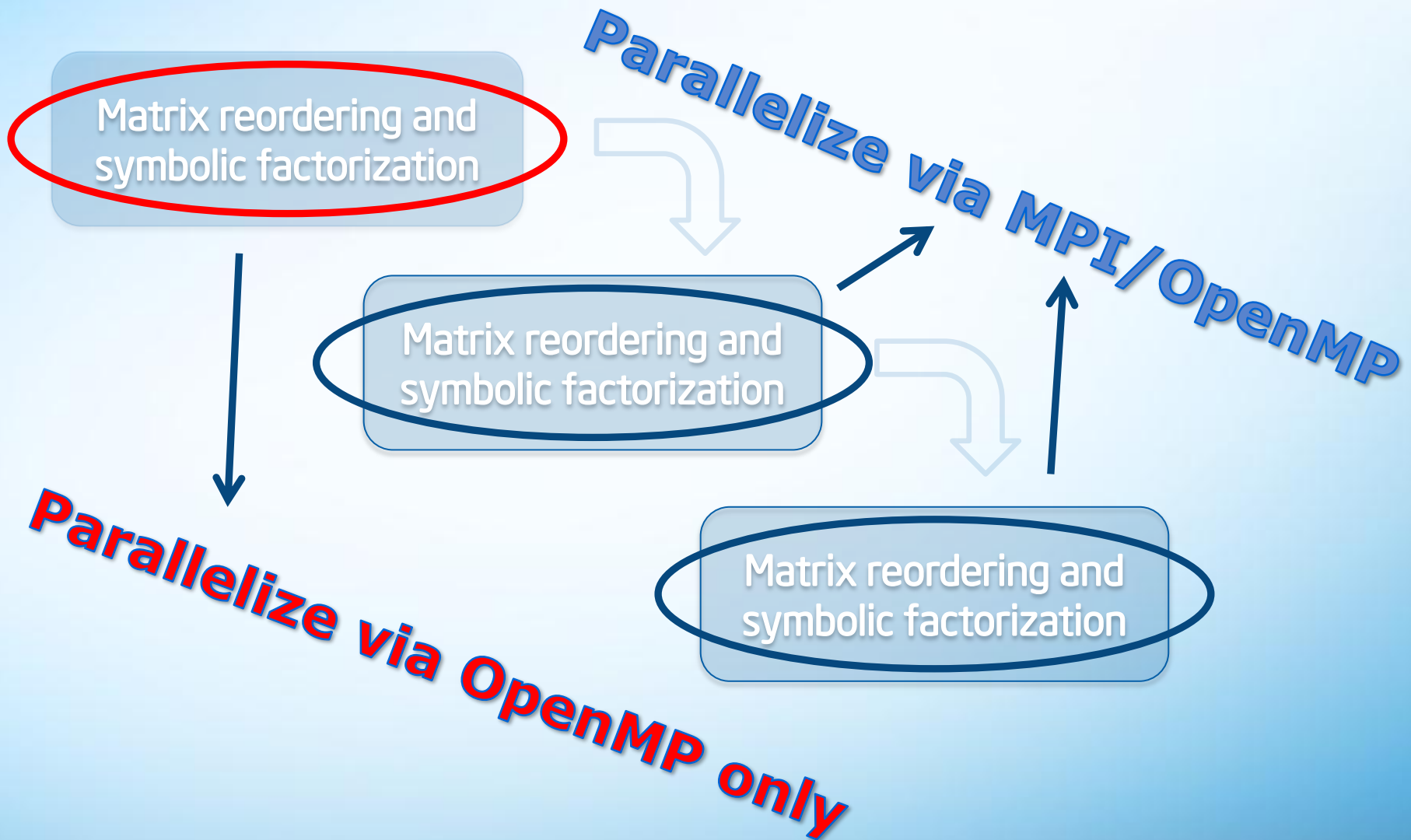


Matrix reordering and
symbolic factorization

Algorithm ($Ax=b$)





Algorithm ($Ax=b$)



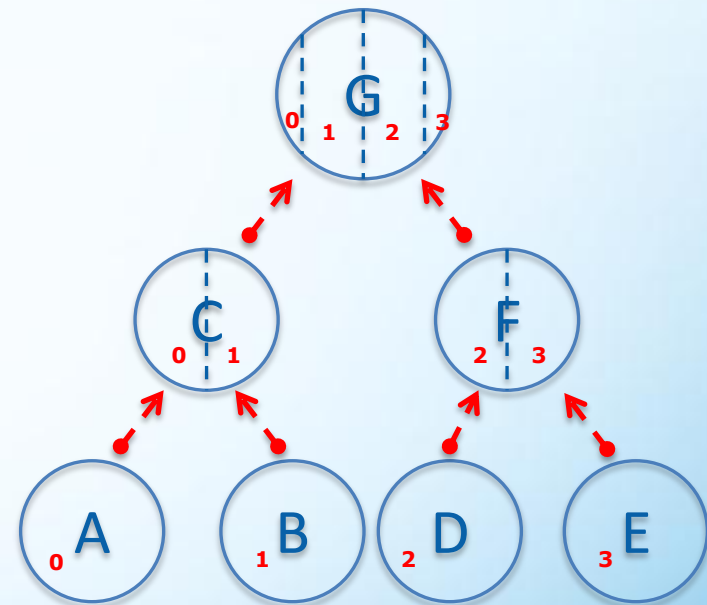
Factorization step

Matrix A after reordering
(example of 4 leafs/processes)

	A	B	C	D	E	F	G
E							
B							
C							
D							
E							
F							
G							

-  - non-zero block
-  - L-block updates R-block
(or Right depends on Left)

Tree representation of
matrix A after reordering



- Both tree and tree-node parallelization are used
- All computations within the node are based on functionality from Intel MKL
- Computation of leafs & updates of a block are independent on each process
- Data is distributed between processes uniformly

Factorization step

Matrix A after reordering
(example of 4 leafs/process)

	A	B	C	D	E	F	G
E							
B							
C							
D							
E							
F							
G							

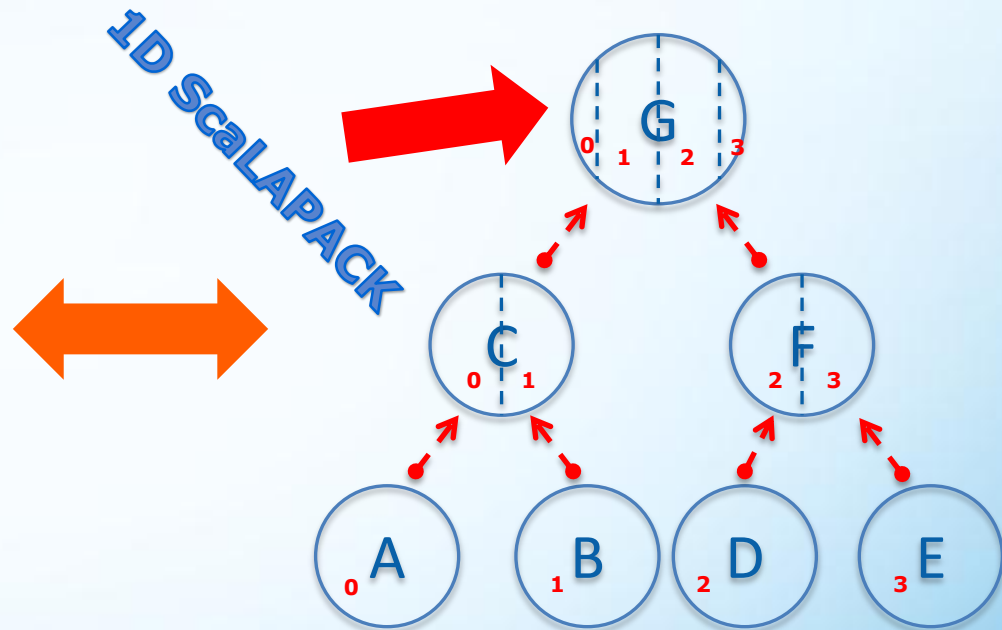


- non-zero block



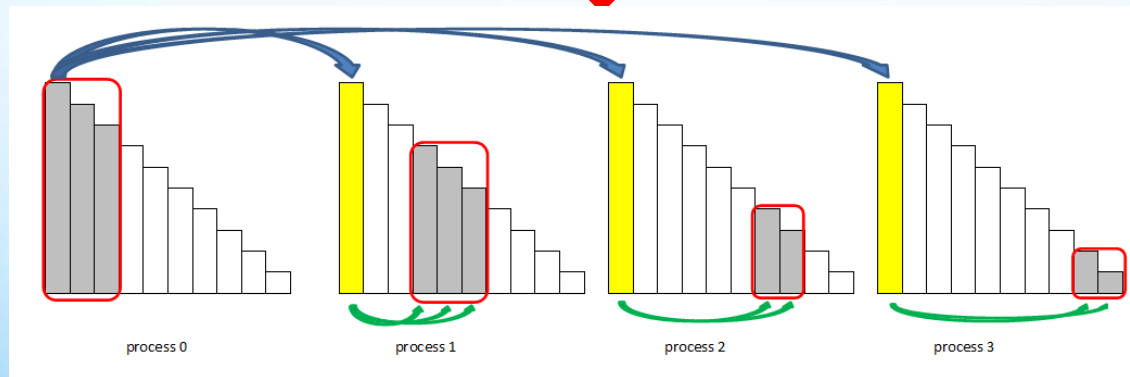
- L-block updates R-block
(or Right depends on Left)

Tree representation of
matrix A after reordering



- Both tree and tree-node parallelization are used
- All computations within the node are based on functionality from Intel MKL
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- Data is distributed between processes uniformly

Implementation of LU decomposition within a "node"



Selecting one thread per process allows us to "mask" data transfers behind computations

Current status/interface

Supports various MPI implementations via BLACs in MKL

C:

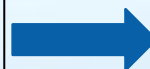
```
{  
....  
  
  PARDISO (pt, &maxfct, &mnum, &mtype,  
           &phase, &n, a, ia, ja, &idum, &nrhs,  
           iparm, &msglvl, b, x, &error);  
  
...  
}
```



```
{  
....  
  
  comm = MPI_Comm_c2f(MPI_COMM_WORLD);  
  CPARDISO (pt, &maxfct, &mnum, &mtype,  
            &phase, &n, a, ia, ja, &idum, &nrhs,  
            iparm, &msglvl, b, x, comm, &error);  
  
...  
}
```

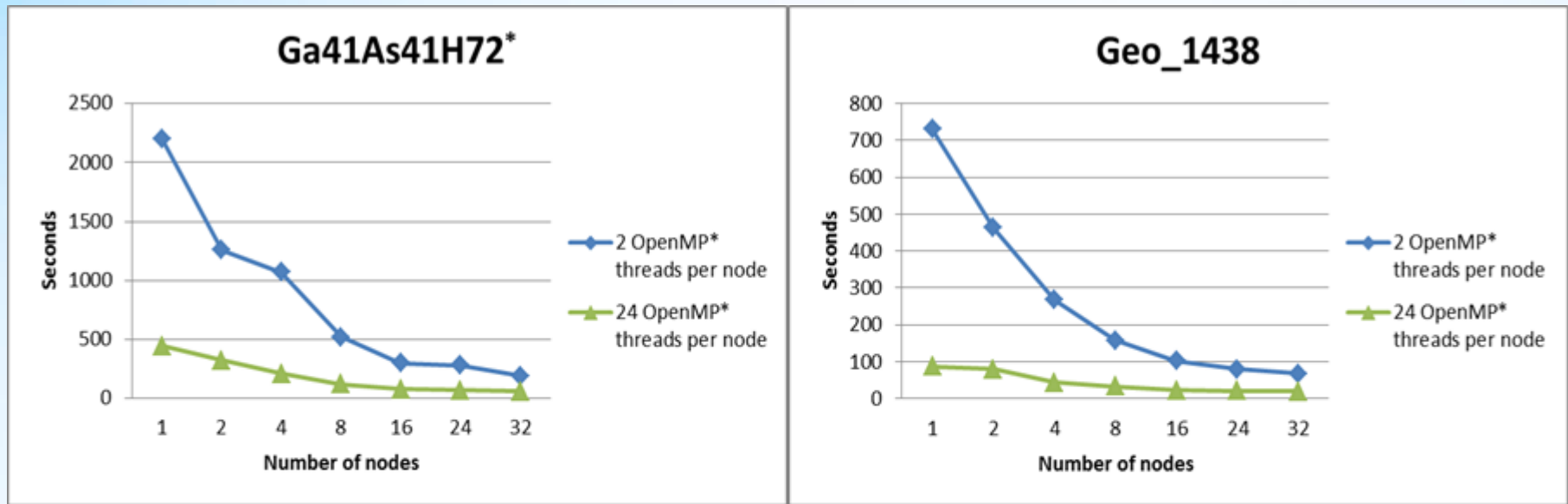
Fortran:

```
....  
  
call PARDISO(pt, maxfct, mnum, mtype,  
             phase, n, a, ia, ja, idum, nrhs,  
             iparm, msglvl, b, x, error)  
  
...
```



```
...  
  
call CPARDISO(pt, maxfct, mnum, mtype,  
              phase, n, a, ia, ja, idum, nrhs,  
              iparm, msglvl, b, x, comm, &error)  
  
...
```

Scalability of Intel MKL Parallel Direct Sparse Solver for Clusters



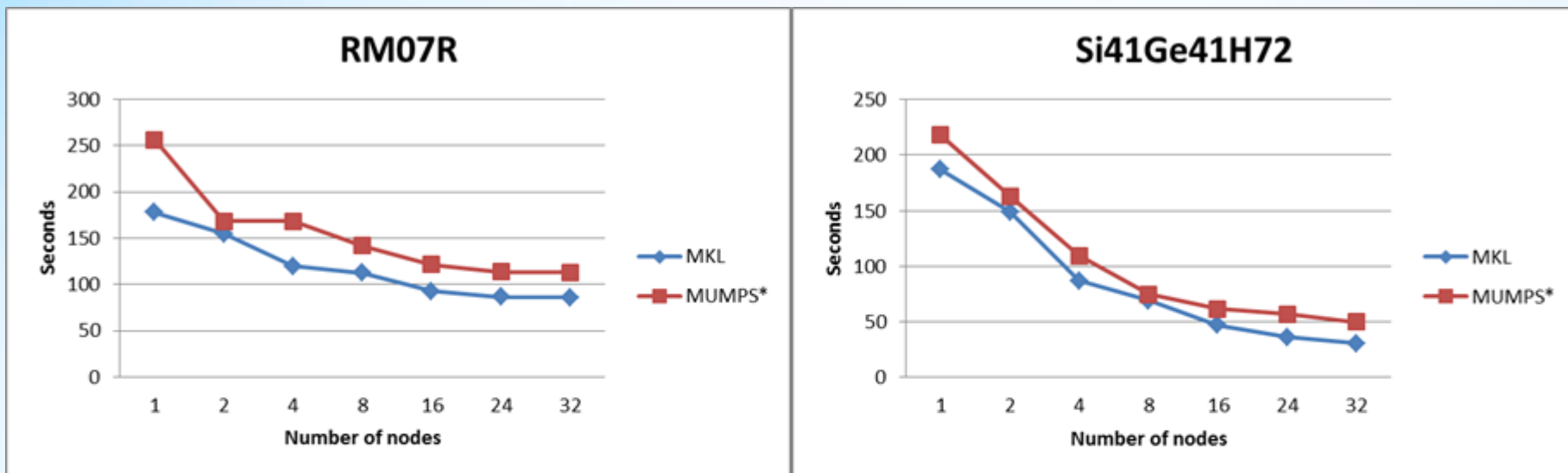
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- Each node contains two Intel® Xeon® E5-2697 v2 processors (24 cores in total), 64GB RAM
- Intel® MKL 11.2 Beta

*Here and further: [The University of Florida Sparse Matrix Collection](#) T. A. Davis and Y. Hu, ACM Transactions on Mathematical Software, Vol 38, Issue 1, 2011, pp 1:1 - 1:25. <http://www.cise.ufl.edu/research/sparse/matrices>.

Scalability comparison



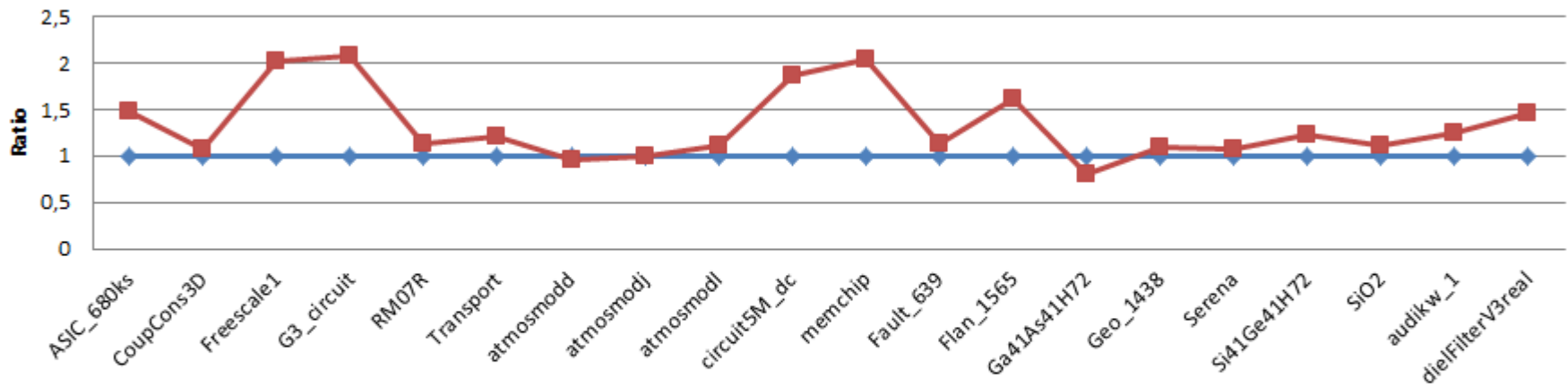
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- Each node contains two Intel® Xeon® E5-2697 v2 processors (24 cores in total), 64GB RAM
- Intel® MKL 11.2 Beta
- MUMPS* version 4.10.0

Performance comparison – Intel MKL speedup over MUMPS*

MUMPS* vs. Intel MKL Parallel Direct Sparse Solver for Clusters (time)
16 nodes 12 threads per node (192 total cores)



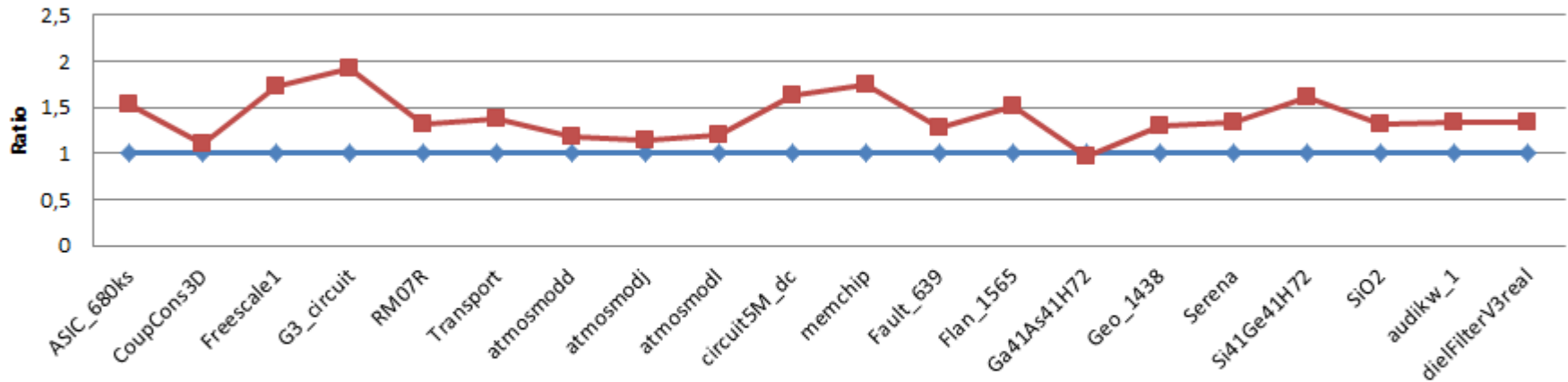
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Performance comparison – Intel MKL speedup over MUMPS*

MUMPS* vs. Intel MKL Parallel Direct Sparse Solver for Clusters (time)
32 nodes 24 threads per node (768 total cores)



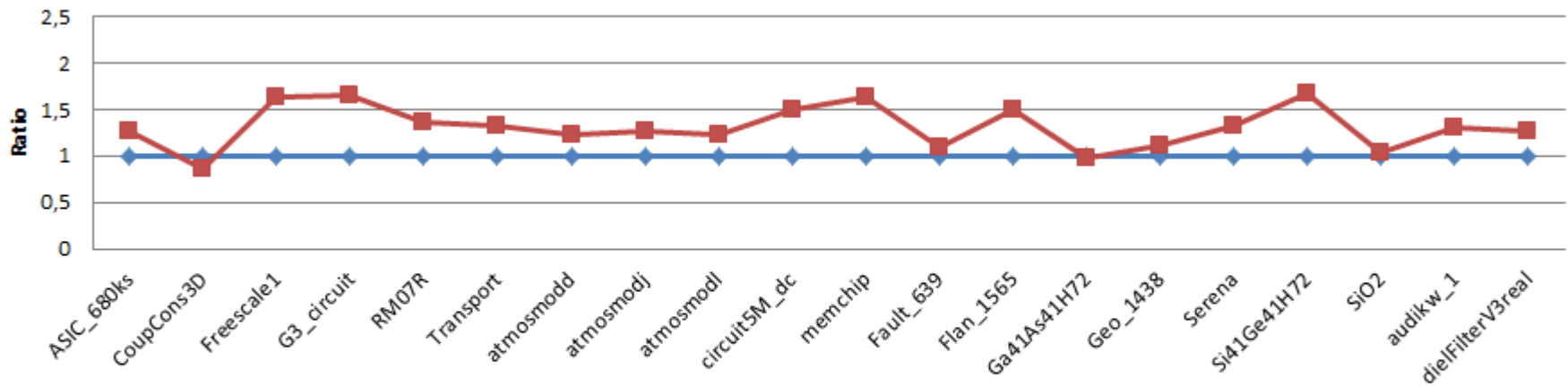
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- Intel® MKL 11.2 Beta
- MUMPS* version 4.10.0

Performance comparison – Intel MKL speedup over MUMPS*

MUMPS* vs. Intel MKL Parallel Direct Sparse Solver for Clusters(time)
64 nodes 24 threads per node (1536 total cores)



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- Intel® MKL 11.2 Beta
- MUMPS* version 4.10.0

Conclusion

- Intel® Direct Sparse Solver for Clusters included in Intel MKL 11.2 Beta functionality results in
 - Good scaling of computational time
 - Good scaling of memory per node
- On the Roadmap:
 - Implement pure MPI version
 - Parallelize reordering step
 - Implement Intel® Xeon Phi™ version

Q & A



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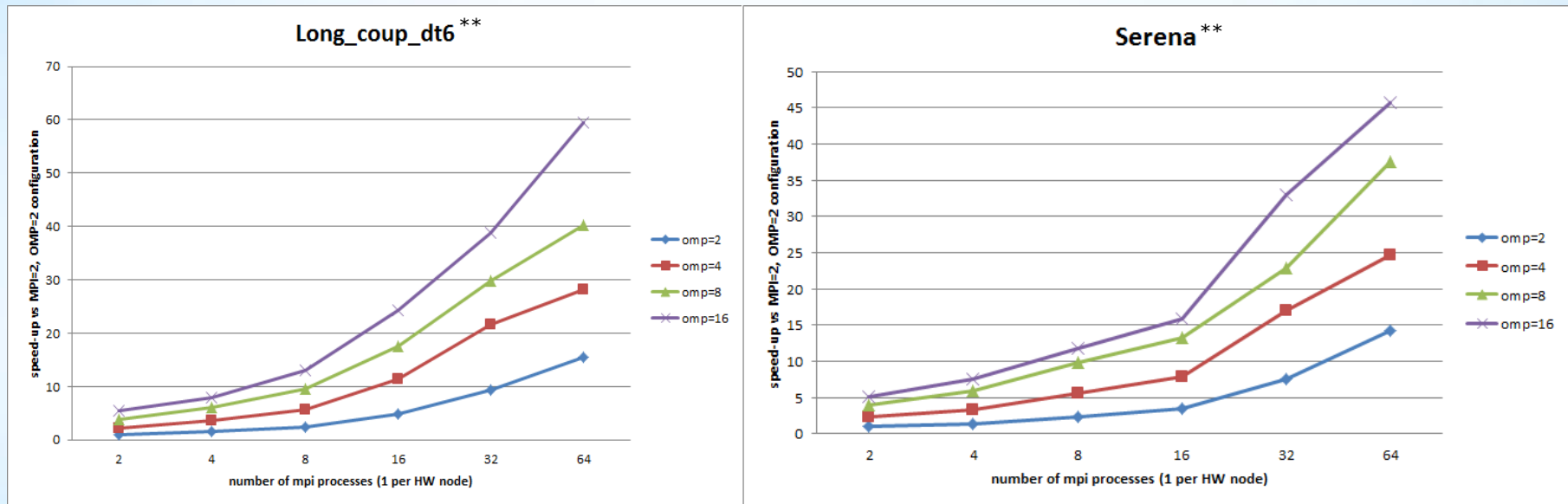
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Experiments (scalability in time)



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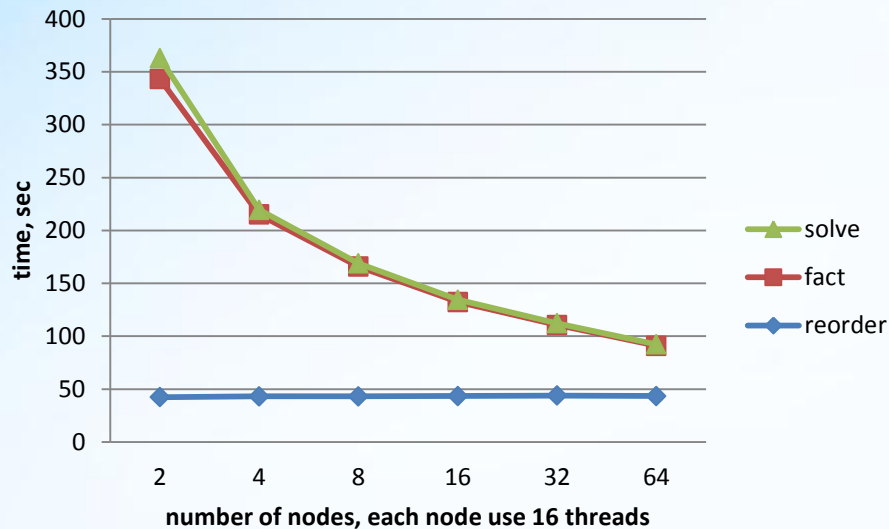
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Experiments (scalability in time)

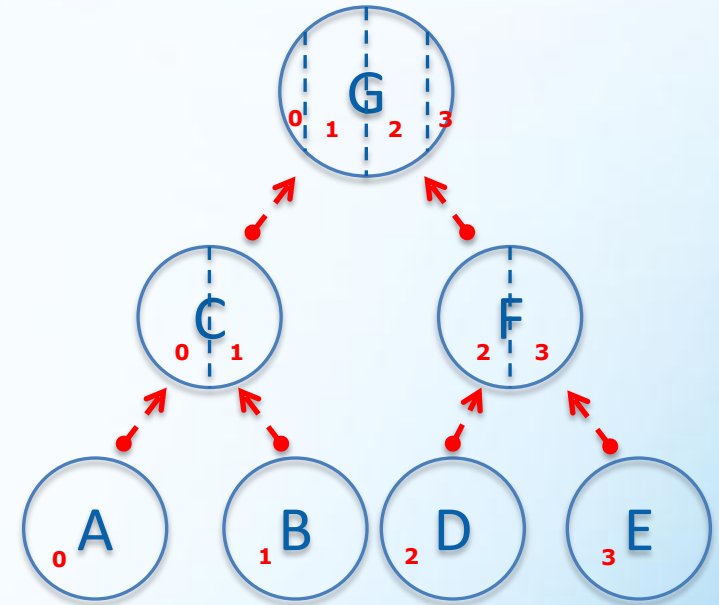
3Dspectralwave^{**}, material problem



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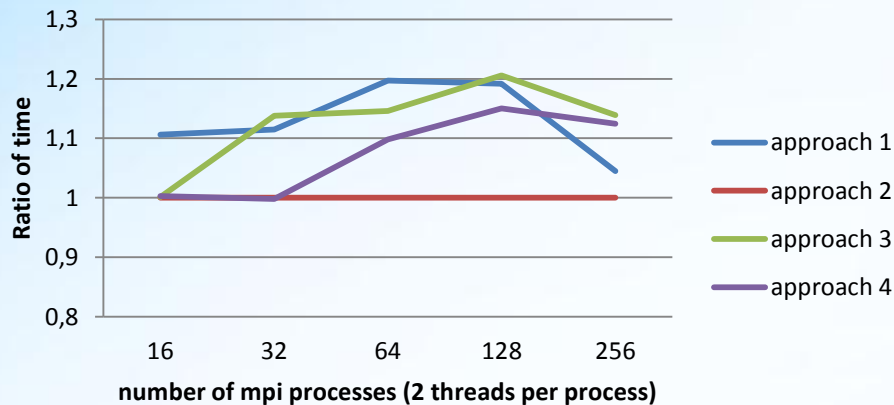


Factorization and solving steps scale well in terms of memory and performance.

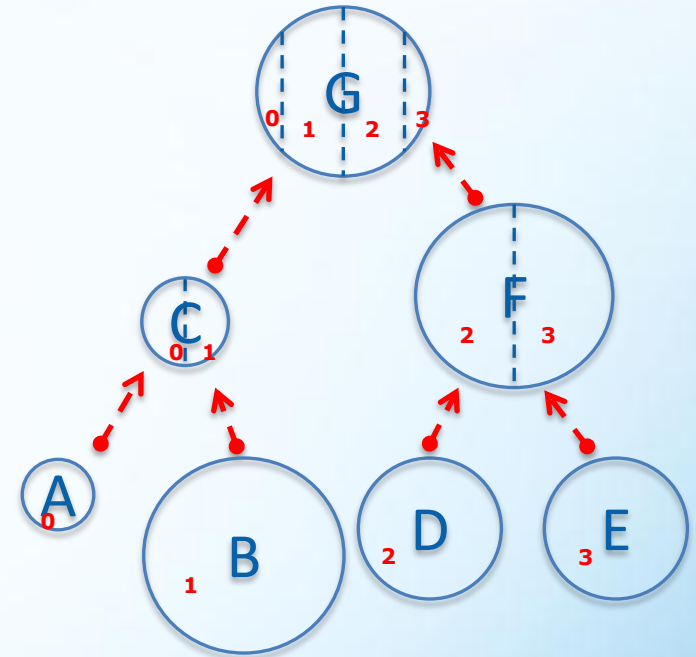
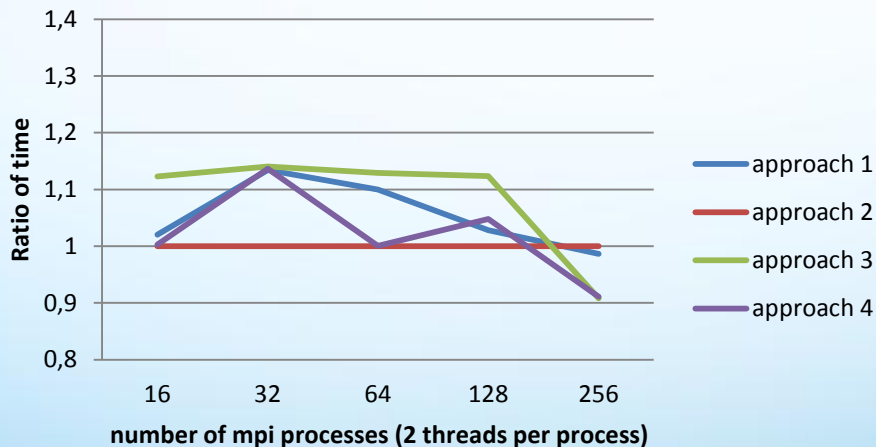
Parallelization of reordering step might lead to “worse” reordering affecting overall time... Deeper investigation is needed here.

Experiments (balancing)

ga41as41h72**



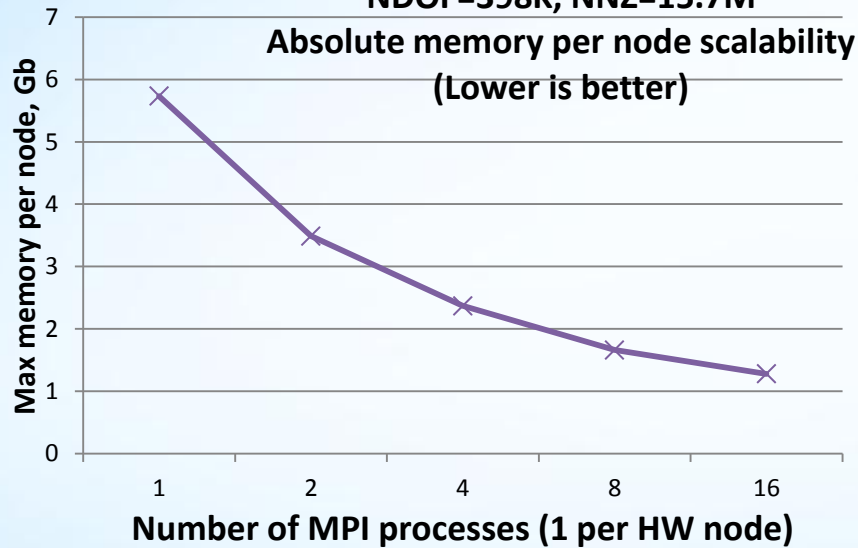
Long_coup_dt6**



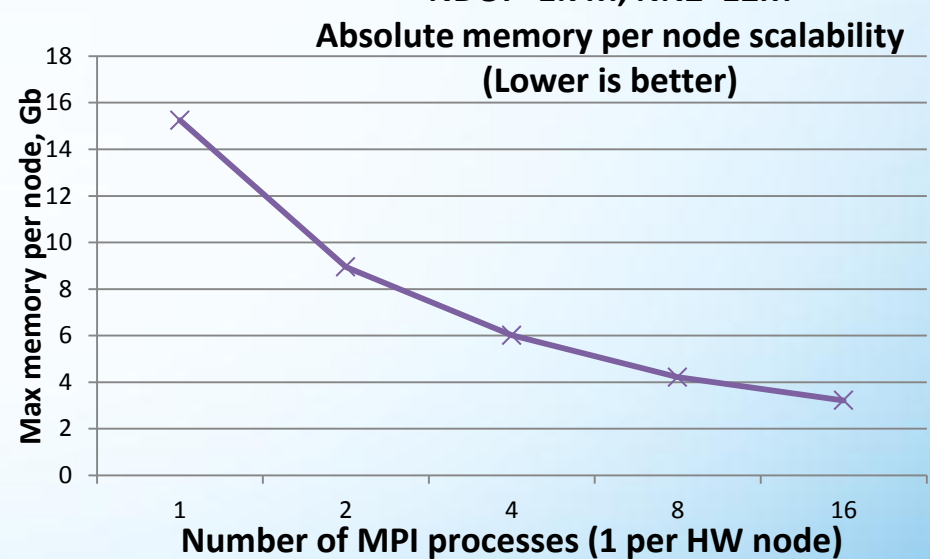
In case of non-uniform "tree", there are a few approaches to divide nodes of the tree between computational nodes. But there is no "best" approach, so to achieve good performance we switch between them at reordering step.

Experiments (scalability on memory)

NDOF=398K, NNZ=15.7M



NDOF=1.7M, NNZ=12M



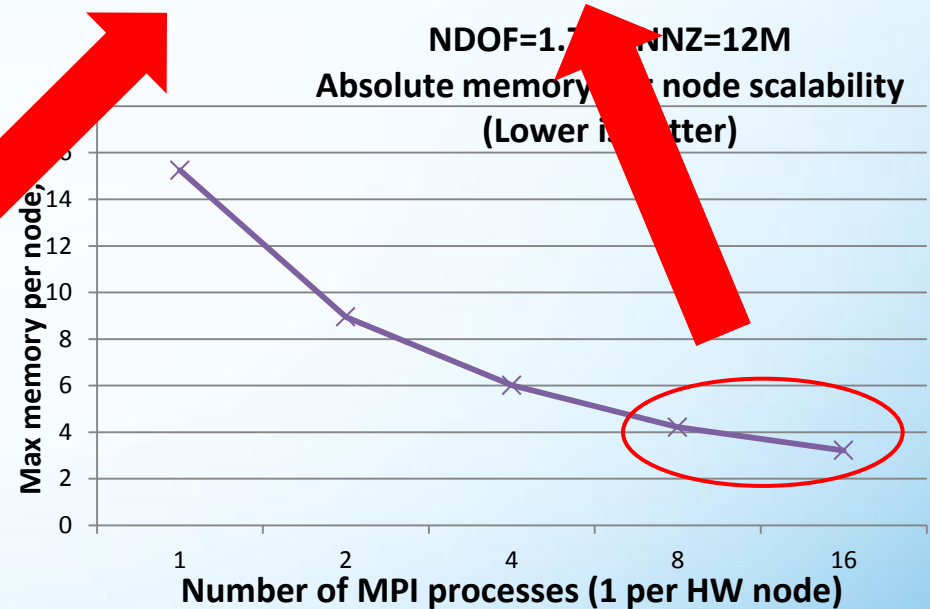
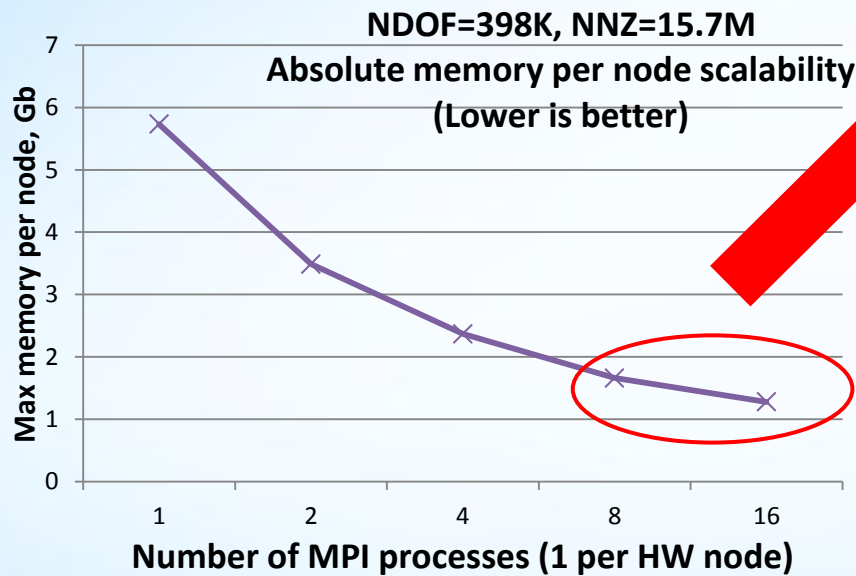
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Experiments (scalability on memory)

***Additional processes
decrease memory
size per host!!!***



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